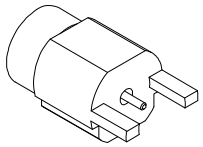
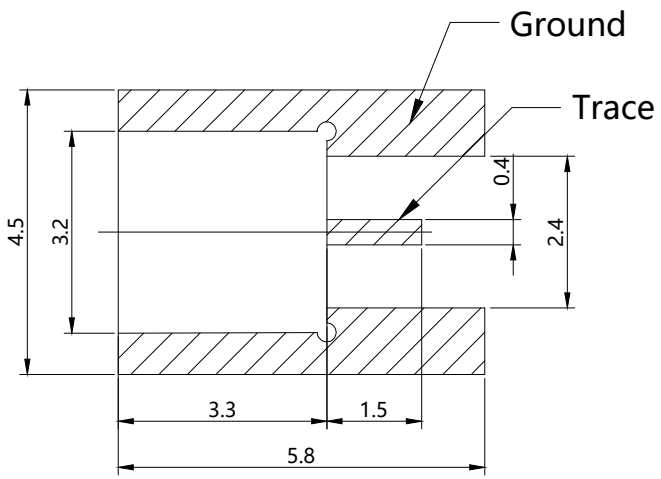
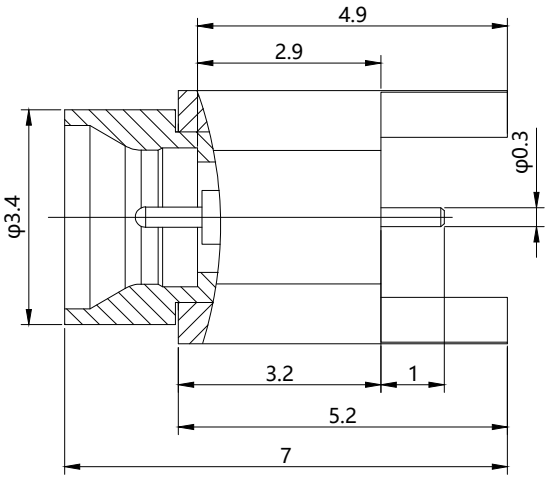
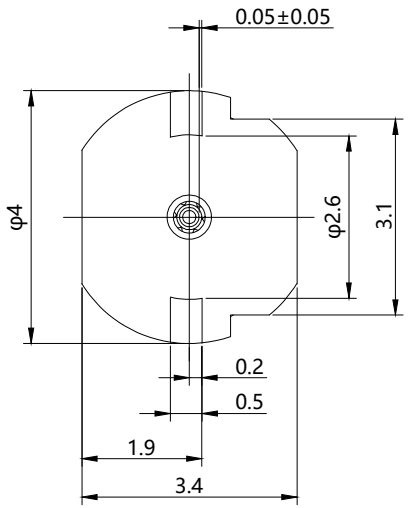
Revisions			
REV:	Description	Date	Approval
A.0			



Recommended PCB Layout







Electrical parameters:
1 Impedance:50Ω
2 Frequency: DC~65GHz
3 VSWR:≤1.5
4 Dielectric withstand voltage: 325Vrms
5 Insulation resistance: 5000MΩ

			General tolerance Unless Otherwise Specified		0 Places;±0.2 / 1 Places;±0.2 / 2 Places;±0.2 / 3 Places;±0.2	4 Places;±0.2 / Angles; / Product:	Shenzhen Vinstronics Electronic Co., Ltd				
Body	Brass	Gold									
Dielectric	PEEK	N/A	Maker	Wang Weitao	Date	2023/04/25	Drawing Number	VST-CPSMPM-07			
Outer conductor	Stainless steel	Gold	Review		Date		description	SMPM Male,Edge Launch, Full Detent			
Center conductor	Beryllium Copper	Gold	Approved		Date						
Description	Material	Plating			Scale:1:5:1	Unit:	mm	RVE	A.0	Page	1 OF 1